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"ring buffer" "search window" macroblock

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»

J MIYAKOSHI... - IEICE Transactions on Electronics, 2005 - IEICE

... the number of pixels on a side of a **macroblock**. ... Sixteen pixels (128 bits) in the **search window** and 16 pixels ... and SRs in one row are connected as a **ring-buffer**. ...

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P Gilhuly - cs.uwaterloo.ca

... 3.2 **Search window** for motion compensation blocks. or saving a video signal to disk, VL provides and manages a **ring buffer** for fields. VL abstracts ...

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"circular buffer" "search area" macroblock

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[L Hui](#)

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JK Tanskanen, T Sihvo, J Niittylahti - Circuits and Systems for Video Technology, IEEE Transactions ..., 2004 - [ieeexplore.ieee.org](#)

... rotated from the end to the beginning of the **circular buffer**. ... on the other hand, we exploit the **search area** needed in coding the previous **macroblock** and we ...

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[Method of motion estimation in mobile device](#)

current application

ST Kim, B Kim, PS Mah - 2005 - [freepatentsonline.com](#)

... and a motion detection **macro block** to be found when a **circular buffer** used in ... 3 shows

a reference **search area** 11 and 12 according to each **macro block** for a ...

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[Cache memory system](#) - all 3 versions »

KH Phong, LYW Hui - US Patent 7,006,100, 2006 - Google Patents

... com/computing/hardware+circular+buffer.* *cited by examiner Primary Examiner ... commonly

employed to store the **search area** and reference **macroblock** to reduce ...

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K PHONG, LYW HUI - EP Patent 1,406,211, 2004 - [freepatentsonline.com](#)

... the cache memory system is arranged to be addressed as a **circular buffer**. ... of current **search area** SA(1,n+1). The **search area** is three **macroblock** columns. ...

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[SPRINT: A Tool to Generate Concurrent Transaction Level Models from Sequential Code](#)

J Cockx, K Denolf, B Vanhoof, R Stahl - [hindawi.com](#)

Page 1. SPRINT: A Tool to Generate Concurrent Transaction Level Models from Sequential Code Johan Cockx, Kristof Denolf, Bart Vanhoof ...

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J Chan, I Laksono - US Patent 7,054,964, 2006 - Google Patents

... a **macroblock** generated from the packets, and then compresses the scaled **macroblock**.

...

Algorithm Based On An Adaptive Reduction of the **Search Area** For Motion ...

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[Method and device for block-based conditional motion compensation](#)

K Denolf - 2003 - [freepatentsonline.com](#)

... the current picture, maximally this **search area** consists of ... 0204] The analysis of

*Phong
et al.*

*filed
November
2004*

denolf

the **macroblock** based flow ... all three cases, a local **circular buffer** limits the ...
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Parallel Memory Architectures for Video Coding

F Draft - digital.dmi.tut.fi

... In this paper, we discussed the re-use of the motion estimation **search-area**.

This **search-area** re-use without parallel memory support ...

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November
2004

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[J Fridman](#)

[B Aldrich](#)

[M Hoffman](#)

[W Anderson](#)

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RK Kolagotla, J Fridman, BC Aldrich, MM Hoffman, ... - Signal Processing Magazine, IEEE, 2002 - [ieeexplore.ieee.org](#)

... The four **circular buffer** addressing registers have associated ... 1 row of the **macro-block** are accumulated ... represents the displacement of the **macroblock** within ...

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[Cache memory system for motion estimation](#)

K PHONG, LYW HUI - EP Patent 1,406,211, 2004 - [freepatentsonline.com](#)

... estimation on two search areas for each reference **macroblock**. ... is arranged to be addressed as a **circular buffer**. ... to support both a large **search window** or two ...

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[Cache memory system - all 3 versions »](#)

KH Phong, LYW Hui - US Patent 7,006,100, 2006 - Google Patents

... [com/computing/hardware+circular+buffer.*](#) *cited by examiner Primary Examiner—Kee M. Tung ... of pixels, each block referred to as a "**macroblock**." Each pixel has ...

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[Transport stream decoder/demultiplexer for hierarchically organized audio-video streams - all 2 versions »](#)

E Washington, M Perkins, B Johnson, S How, N ... - US Patent 5,920,572, 1999 - Google Patents

... FIG. A (PRIOR ART) CURRENT **MACROBLOCK** MOTION VECTOR SUBSEQUENT PICTURE **SEARCH**

--**WINDOW**-BEST MATCHING **MACROBLOCK** PRESENTATION ORDER Page 6. US Patent Jul. ...

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J Chan, I Laksono - US Patent 7,054,964, 2006 - Google Patents

... a **macroblock** generated from the packets, and then compresses the scaled **macroblock**.

...

Lee, Liang-Wei et al., "Dynamic **Search-Window** Adjust -ment and Interlaced ...

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[Energy Efficient Support for All Levels of Parallelism for Complex Media Applications - all 2 versions »](#)

R Sasanka - 2005 - [rsim.cs.uiuc.edu](#)

... for a "best- matching" **macroblock** within a **search window** in a previously encoded frame. The distance or "match" between ...

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[DSP For Handsets: The Blackfin Processor](#)

J Fridman, Z Zvonar - [doi.wiley.com](#)

... The address register file consists of six 32-bit general-purpose pointer registers and four 32-bit **circular buffer** addressing registers. ...

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J MIYAKOSHI... - IEICE Transactions on Electronics, 2005 - IEICE

... represents the number of pixels on a side of a macroblock. A search range is $0 < x \leq 2dx$, $0 < y \leq 2dy$... PEs and SRs in one row are connected as a ring-buffer. ...

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[Method of encoding image signal, apparatus for encoding image signal, method of decoding image ... - all 3 versions »](#)

Y Yagasaki, J Yonemitsu, T Suzuki - US Patent 5,786,858, 1998 - Google Patents

... Frame Number FI G. 1 8 10 11 DCT Unit Quinrentjai y Signal Encoder Motion Vector
|Ã~ ICounter p Macroblock Number FIG.1A ROM T 55 1r>4 Dequa- ntizer nl ...

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